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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/734,020	12/10/2003	Jon C. Lee	N1085-00183	1843
8933	7590 10/05/2004		EXAMINER	
DUANE MORRIS, LLP			KESHAVAN, BELUR V	
IP DEPARTI ONE LIBER	·· ·· -·-		ART UNIT	PAPER NUMBER
• · · -	PHIA, PA 19103-7396		2825	<u> </u>
		•	DATE MAILED: 10/05/200	4

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	7/			
	10/734,020	LEE ET AL.				
Office Action Summary	Examiner	Art Unit	<u>-</u>			
	Belur V Keshavan	2825				
The MAILING DATE of this communication ap Period for Reply	ppears on the cover sheet w	th the correspondence address				
A SHORTENED STATUTORY PERIOD FOR REP	LY IS SET TO EXPIRE 3 M	ONTH(S) FROM				
THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a re - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a r ply within the statutory minimum of thir d will apply and will expire SIX (6) MON te, cause the application to become AE	eply be timely filed ty (30) days will be considered timely. ITHS from the mailing date of this communication BANDONED (35 U.S.C. § 133).	оп.			
Status						
1) Responsive to communication(s) filed on 10	December 2003.					
2a)☐ This action is FINAL . 2b)☑ Th	is action is non-final.					
3) Since this application is in condition for allow	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D.	. 11, 453 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-20 is/are pending in the applicatio	n.					
4a) Of the above claim(s) is/are withdra	awn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-20</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/	or election requirement.					
Application Papers						
9) The specification is objected to by the Examir	ier.					
10)⊠ The drawing(s) filed on 10 December 2003 is/	10)⊠ The drawing(s) filed on 10 December 2003 is/are: a)⊠ accepted or b)□ objected to by the Examiner.					
Applicant may not request that any objection to the	e drawing(s) be held in abeyar	ice. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the corre	ction is required if the drawing	(s) is objected to. See 37 CFR 1.121(d).			
11) The oath or declaration is objected to by the E	Examiner. Note the attached	Office Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12)☐ Acknowledgment is made of a claim for foreig	n priority under 35 U.S.C. §	119(a)-(d) or (f).				
a) All b) Some * c) None of:						
 Certified copies of the priority documer 	nts have been received.					
2. Certified copies of the priority documer	nts_have_been_received_in_A	pplication No				
3. ☐ Copies of the certified copies of the pri	•	received in this National Stage				
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a lis	it of the certified copies not	received.				
Attachment(s)	_					
Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Lanterview S	summary (PTO-413) s)/Mail Date				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08	3) 5) 🔲 Notice of Ir	nformal Patent Application (PTO-152)				
Paper No(s)/Mail Date	6) 🔲 Other:					

DETAILED ACTION

Page 2

Claim Objection

Claims 10 and 13 are objected under 37 CFR 1.75(d).

There is insufficient antecedent basis for "the chip" in claim 10 line 7 and in claim 13 lines 1-2.

The examiner suggests changing "the chip" in claims 10 and 13 to --the substrate--.

Any further rejections of, or indications of the allowability of, any claims 10-20 are based on claims 10-20 as they are understood by the examiner.

Double Patenting

Warning

Applicant is advised that should claims 1, 10 and their respective dependencies be found allowable, claims 10-19 will be objected to under 37 CFR 1.75 as being a substantial duplicate of claims 1-9 and 20 thereof. When two claims in an application are duplicates or else are so close in content that they both cover the same thing, despite a slight difference in wording, it is proper after allowing one claim to object to the other as being a substantial duplicate of the allowed claim. See MPEP § 706.03(k).

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Application/Control Number: 10/734,020

Art Unit: 2825

Claims 1-7, 9-16, and 18-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Maeda et al. (U. S. Pub. No. 2003/0057988)

Regarding claims 1-7, 9-16, and 18-20 Maeda et al. teach on page 2, paragraph [0021], and in figure 1 a method for measuring current leakage of a contact (92) of a semiconductor device (5) formed in a substrate (4), the method comprising the steps of:

Exposing the contact (94) known to be defective thereby marking the contact before scanning with an electrically conductive tip probe (3) of a conductive atomic force microscope; applying a DC voltage (2) from a variable DC voltage source thereby being able to apply at least two voltages between the substrate (4) attached to a holder (paragraph [0029]) and the tip of the probe; and measuring currents (1) passing through the contacts to the substrate, in response to various applied variable DC voltages (2) using an ammeter.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 8 and 17 is rejected under 35 U.S.C. 103(a) as being unpatentable over Maeda et al. (U. S. Pub. No. 2003/0057988) in view of B. El-Kareh (Introduction to VLSI Silicon Devices, page 93).

Regarding claims 8 and 17, Maeda et al teach all of the limitations of the base claim but lacks plotting the values of current against the values of the applied voltage. However it is very well known in the art of which the examiner takes official notice to plot the values of current

Application/Control Number: 10/734,020

Art Unit: 2825

against voltage. In support of this assertion the examiner cites a textbook by B. El-Kareh

(Introduction to VLSI Silicon Devices, page 93).

Contact Information

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Belur V Keshavan whose telephone number is 571-272-1894.

The examiner can normally be reached on 8-4:30 Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Matthew S Smith can be reached on 571-272-1907. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

BVK. YXL.

September 30, 2004.

Belur V. Keshavan.

Examiner. Art Unit 2825.

Page 4

MATTHEW SMITH SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2800